

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on line 15 of page 3 of the specification according to the following marked up paragraph.

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- 5 During the normal simulation, the microcontroller and the peripheral module are simulated with a precise clock cycle, whereas in the accelerated code execution, precise "simulated" time does not elapse; that is, the program part is processed in a kind of instruction set simulator.
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